

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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MAY 12 2004

In re Application of

Yoshinori Uchiyama

Serial No.: 09/893,598 Group Art Unit: 2675

Filed: June 29, 2001 Examiner: Bell, Paul A.

For: SEMICONDUCTOR CIRCUIT IN WHICH POWER CONSUMPTION IS  
REDUCED AND SEMICONDUCTOR CIRCUIT SYSTEM USING THE SAMEHonorable Commissioner of Patents  
Alexandria, VA 22313-1450

OFFICIAL

**SUPPLEMENTAL AMENDMENT UNDER 37 C.F.R. §1.111**

Sir:

Further the Amendment Under 37 CFR §1.111, filed on February 25, 2004, please amend the above-identified application, as follows.

**INITIAL REMARKS:**

Claims 1, 15, 20 have been amended at the Examiner's request to eliminate the requirement to import a "negative limitation" into the claims, as based on the argument of the above-referenced Amendment.

Claims 6, 11, and 18 have been amended to address an antecedent basis identified by the Examiner during personal interview dated April 28, 2004, and claim 10 has been amended to clarify the language in accordance with the Examiner's concern. Claim 3 has been amended for simpler format.

The attached appendix provides an exemplary correlation between claim language and the figures, as requested by the Examiner during the interview. Such correlation is only for the Examiner's clarity and not for limiting the invention in any manner, let alone limiting the invention to what is specifically shown in the figures. The reference numerals used are for exemplary purposes only.